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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Daniel William BAILEY	§	
		§	
Serial No.:	09/988,691	§	Group Art Unit: 2631
		§	
Filed:	November 20, 2001	§	Examiner: UNKNOWN
		§	
For:	Expanded Comparator For	§	
	Control Of Digital Delay	§	
	Lines In A Delay Locked Loop	§	
	Or Phase Locked Loop	§	

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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents  
Washington, D.C. 20231

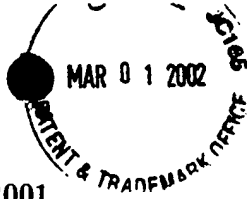
Att'y. Docket No. 1662-37600  
Client Docket No. P00-3208  
Date: February 7, 2002

Sir:

This Information Disclosure Statement, including completed Form PTO-1449, comprises a list of pertinent art of which Applicant is aware. A copy of each publication listed on Form PTO-1449 is enclosed herewith.

The submission of this Information Disclosure Statement and the references submitted therewith is not an admission that the art cited is "prior" with respect to the present invention, nor is it a representation that no better art exists. Applicant hereby reserves the right to swear behind or otherwise disprove any alleged "prior" nature of any art cited should the facts support and the situation warrant such an action. It is submitted that the art cited does not constitute a bar to the patentability of Applicant's invention under 35 U.S.C. § 102 or § 103.

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This Information Disclosure Statement is being filed within three (3) months of filing date.  
Therefore, no fee is due for the filing of these papers.

Respectfully submitted,

A handwritten signature in cursive script, reading "Jonathan M. Harris".

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Group  
2631

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Page 1 of 1